STEP DRIVER 2M542



Key Features:

- 20 to 50VDC Supply Voltage
- ☐ H-Bridge, 2 Phase Bi-polar Micro-stepping Drive
- ☐ Suitable for 2-phase, 4, 6 and 8 leads step motors, with Nema size 16 to 34
- ☐ Output current selectable from 1.0 ~ 4.2A peak
- ☐ Current reduction by 50% automatically, when motor standstill mode is enabled
- ☐ Pulse Input frequency up to 200 kHz
- Optically isolated differential TTL inputs for Pulse,
 Direction and Enable signal inputs
- ☐ Selectable resolutions up to 25000 steps
- □ Over Voltage, Coil to Coil and Coil to Ground short circuit protection.

Introduction

2M542 is a cost effective, high performance bi-polar two phase micro-stepping drive applying pure-sinusoidal current control technique. It is best suited for the applications that desired extreme low noise and heat. It operates well in an environment, where electricity supply experience instability and fluctuation.

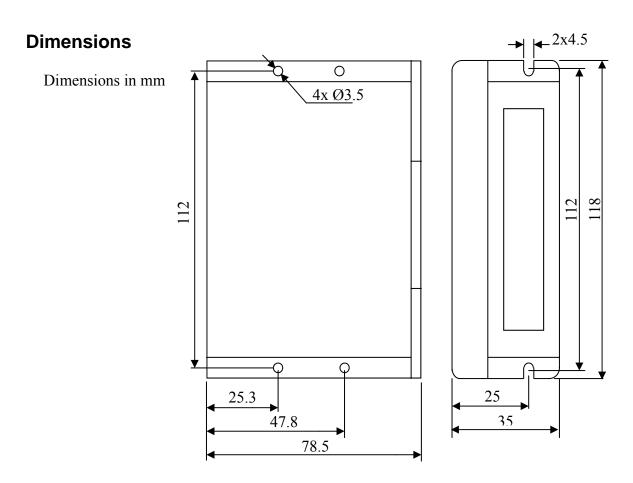
The general pseudo-sinusoidal current control technology adopted by majority of the drive produced distorted sine wave, and current ripple, resulting in vibration, noise and motor heating. This results in motor degrading over time, reducing in motor performance and shortens the usage life.

With an automatic optimization speed control technique, the PowerStep series drive output is very stable, with almost zero vibration and noise, performing close to a servo system, allowing the motor to operate smoothly. That helps to fulfill a design requirement of low noise, low heat and high performance.

Specifications

Parameters	Min	Typical	Max	Unit
Output Current (Peak)	1.0	-	4.2	Amps
Supply voltage	20	36	50	VDC
Logic Input Current	7	10	16	mA
Pulse input frequency	0	-	200	KHz
Low Level Time	2.5			μsec

Cooling	Natural Cooling or Forced Convection		
	Space	Avoid dust, oil frost and corrosive gases	
Environment	Ambient Temperature	0°C – 50°C	
	Humidity	40 - 80%RH	
	Vibration	5.9m/s² Max	
Storage Temp.	-10°C -80°C		
Weight	Approx. 260 gram		



Current Setting

Current Setting (A)	SW1	SW2	SW3	
1.0	ON	ON	ON	
1.46	OFF	ON	ON	
1.91	ON	OFF	ON	
2.37	OFF	OFF	ON	
2.84	ON	ON	OFF	
3.31	OFF	ON	OFF	
3.76	ON	OFF	OFF	
4.20	OFF	OFF	OFF	

Microstep Setting

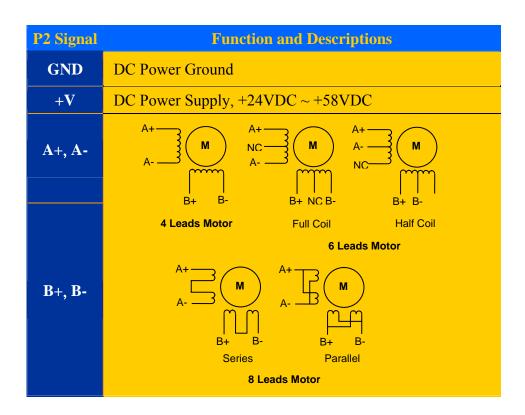
Microstep resolution	Step / Rev	SW5	SW6	SW7	SW8
2	400	OFF	ON	ON	ON
4	800	ON	OFF	ON	ON
8	1600	OFF	OFF	ON	ON
16	3200	ON	ON	OFF	ON
32	6400	OFF	ON	OFF	ON
64	12800	ON	OFF	OFF	ON
128	25600	OFF	OFF	OFF	ON
5	1000	ON	ON	ON	OFF
10	2000	OFF	ON	ON	OFF
20	4000	ON	OFF	ON	OFF
25	5000	OFF	OFF	ON	OFF
40	8000	ON	ON	OFF	OFF
50	10000	OFF	ON	OFF	OFF
100	20000	ON	OFF	OFF	OFF
125	25000	OFF	OFF	OFF	OFF

^{*} SW4: ON=Full current, SW4 : OFF=Half current

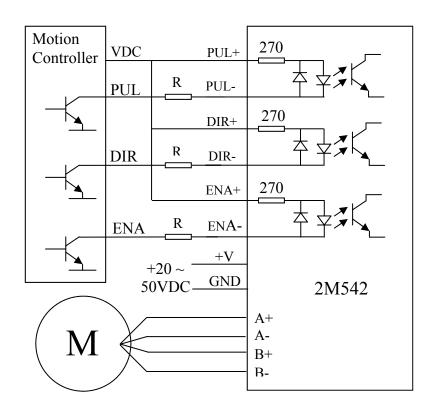
P1 Pin Assignment

Signal	Function and Descriptions
PUL+	Pulse or Step Input TTL differential input with high-going pulse, 1 μs min width. For +12V or +24V operation, a current limiting
PUL-	resistor had to be pull up or connected in series from the PUL+ to the VCC.
DIR+	<u>Direction Input</u> Logic High = positive (CW) rotation— $4.0 \sim 5.0$ V Logic Low = negative (CCW) rotation— $0 \sim 0.5$ V
DIR-	The DIR signal must be stable for at least 5ms before the drive receives the first pulse.
ENA+	Enable Input Logic High = Drive Enabled Logic Low = Drive Disabled
ENA-	This input, if left unconnected, is regconised as Logic High by the drive, and it will be enabled.

P2 Pin Assignment



Wiring



R=0 if VDC=5V

R=1K(Power>0.125W) if VDC=12V;

R=2K(Power>0.125W) if VDC=24V;

R must be connected to control signal terminal.