

**7300CV PLC MEMORY MAP**

Ladder Code(0-20 lines)	200h~231h
Ladder Code(21-40 lines)	232h~263h
Timer Code	264h~28Bh
Counter Code	28Ch~29Bh
Reserved	29Ch~2ABh
Encoder Code	2ACh~2BFh
Analog Code	2C0h~2CBh
Run control Code	2CCh~2FBh

**Final output detection (for ladder to determine)**

Reserved	2FCh
Reserved	
Input Bit (S1~S6)	2FDh
Reserved	
Timer Bit (T1~T8)	2FEh
Reserved	
Counter Bit (C1~C4)	2FFh
Reserved	
Auxiliary coil (M1~M8)	300h
Auxiliary coil (M9~M15)	
Run control (F1~F8)	301h
Reserved	
Analog Bit (G1~G4)	302h
Encoder bit (H1~H4)	
Output Bit (Q1~Q2)	303h
Reserved	
Compile machine Code	304h~ 32Bh

Note:

Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0
8	7	6	5	4	3	2	1

## Trigger detection (for Function to determine)

Encoder Bit	32Ch
Analog Bit	
Timer Bit (present)	32Dh
Timer Bit (last time)	
Counter Bit (present)	32Eh
Counter Bit (last time)	
Run control Bit	32Fh

## PLC Run Command

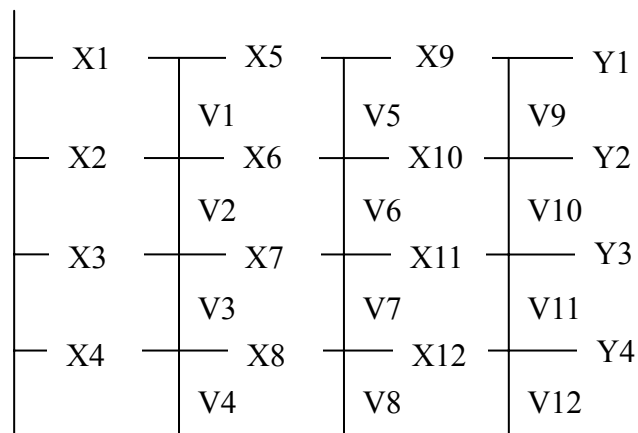
RUN&STOP	330h
Clear all memory	331h

## Ladder's Component arrangement

**X1---X12: Contact**

**Y1---Y4: Coil**

**V1---V12: Vertical line**



**Arrangement of code in EEPROM/RAM****A. Ladder Part** ( note: L =Low byte , H = High byte )

Page	Address	Location	Code								
1	200h	X1	( Byte )	C3	C2	C1	C0	N3	N2	N1	N0
		X2	( Byte )	C3	C2	C1	C0	N3	N2	N1	N0
	201h	X3	( Byte )	C3	C2	C1	C0	N3	N2	N1	N0
		X4	( Byte )	C3	C2	C1	C0	N3	N2	N1	N0
	202h	X5	( Byte )	C3	C2	C1	C0	N3	N2	N1	N0
		X6	( Byte )	C3	C2	C1	C0	N3	N2	N1	N0
	203h	X7	( Byte )	C3	C2	C1	C0	N3	N2	N1	N0
		X8	( Byte )	C3	C2	C1	C0	N3	N2	N1	N0
	204h	X9	( Byte )	C3	C2	C1	C0	N3	N2	N1	N0
		X10	( Byte )	C3	C2	C1	C0	N3	N2	N1	N0
	205h	X11	( Byte )	C3	C2	C1	C0	N3	N2	N1	N0
		X12	( Byte )	C3	C2	C1	C0	N3	N2	N1	N0
	206h	Y1	( Byte )	C3	C2	C1	C0	N3	N2	N1	N0
		Y2	( Byte )	C3	C2	C1	C0	N3	N2	N1	N0
	207h	Y3	( Byte )	C3	C2	C1	C0	N3	N2	N1	N0
		Y4	( Byte )	C3	C2	C1	C0	N3	N2	N1	N0
	208h	V1~ V4		V4	V3	V2	V1	0	0	0	0
		V5~ V8		V8	V7	V6	V5	0	0	0	0
	209h	V9~V12		V12	V11	V10	V9	0	0	0	0
		reserved		0	0	0	0	0	0	0	0

# 7300CV PLC Function Code Rule

NO :  
version : 0.8  
page : 4

Page	Address	Location
2	20Ah	X1
		X2
	20Bh	X3
		X4
	20Ch	X5
		X6
	20Dh	X7
		X8
	20Eh	X9
		X10
	20Fh	X11
		X12
	210h	Y1
		Y2
	211h	Y3
		Y4
	212h	V1~V4
		V5~V8
	213h	V9~V12
		reserved

Page	Address	Location
3	214h	X1
		X2
	215h	X3
		X4
	216h	X5
		X6
	217h	X7
		X8
	218h	X9
		X10
	219h	X11
		X12
	21Ah	Y1
		Y2
	21Bh	Y3
		Y4
	21Ch	V1~V4
		V5~V8
	21Dh	V9~V12
		reserved

Page	Address	Location
4	21Eh	X1
		X2
	21Fh	X3
		X4
	220h	X5
		X6
	221h	X7
		X8
	222h	X9
		X10
	223h	X11
		X12
	224h	Y1
		Y2
	225h	Y3
		Y4
	226h	V1~V4
		V5~V8
	227h	V9~V12
		reserved

Page	Address	Location
5	228h	X1
		X2
	229h	X3
		X4
	22Ah	X5
		X6
	22Bh	X7
		X8
	22Ch	X9
		X10
	22Dh	X11
		X12
	22Eh	Y1
		Y2
	22Fh	Y3
		Y4
	230h	V1~V4
		V5~V8
	231h	V9~V12
		reserved

# 7300CV PLC Function Code Rule

NO :  
version : 0.8  
page : 5

Page	Address	Location	Code								
6	232h	X1	( Byte )	C3	C2	C1	C0	N3	N2	N1	N0
		X2	( Byte )	C3	C2	C1	C0	N3	N2	N1	N0
	233h	X3	( Byte )	C3	C2	C1	C0	N3	N2	N1	N0
		X4	( Byte )	C3	C2	C1	C0	N3	N2	N1	N0
	234h	X5	( Byte )	C3	C2	C1	C0	N3	N2	N1	N0
		X6	( Byte )	C3	C2	C1	C0	N3	N2	N1	N0
	235h	X7	( Byte )	C3	C2	C1	C0	N3	N2	N1	N0
		X8	( Byte )	C3	C2	C1	C0	N3	N2	N1	N0
	236h	X9	( Byte )	C3	C2	C1	C0	N3	N2	N1	N0
		X10	( Byte )	C3	C2	C1	C0	N3	N2	N1	N0
	237h	X11	( Byte )	C3	C2	C1	C0	N3	N2	N1	N0
		X12	( Byte )	C3	C2	C1	C0	N3	N2	N1	N0
	238h	Y1	( Byte )	C3	C2	C1	C0	N3	N2	N1	N0
		Y2	( Byte )	C3	C2	C1	C0	N3	N2	N1	N0
	239h	Y3	( Byte )	C3	C2	C1	C0	N3	N2	N1	N0
		Y4	( Byte )	C3	C2	C1	C0	N3	N2	N1	N0
	23Ah	V1~ V4		V4	V3	V2	V1	0	0	0	0
		V5~ V8		V8	V7	V6	V5	0	0	0	0
	23Bh	V9~V12		V12	V11	V10	V9	0	0	0	0
		reserved		0	0	0	0	0	0	0	0

# 7300CV PLC Function Code Rule

NO :  
version : 0.8  
page : 6

Page	Address	Location
7	23Ch	X1
		X2
	23Dh	X3
		X4
	23Eh	X5
		X6
	23Fh	X7
		X8
	240h	X9
		X10
	241h	X11
		X12
	242h	Y1
		Y2
	243h	Y3
		Y4
	244h	V1~V4
		V5~V8
	245h	V9~V12
		reserved

Page	Address	Location
8	246h	X1
		X2
	247h	X3
		X4
	248h	X5
		X6
	249h	X7
		X8
	24Ah	X9
		X10
	24Bh	X11
		X12
	24Ch	Y1
		Y2
	24Dh	Y3
		Y4
	24Eh	V1~V4
		V5~V8
	24Fh	V9~V12
		reserved

Page	Address	Location
9	250h	X1
		X2
	251h	X3
		X4
	252h	X5
		X6
	253h	X7
		X8
	254h	X9
		X10
	255h	X11
		X12
	256h	Y1
		Y2
	257h	Y3
		Y4
	258h	V1~V4
		V5~V8
	259h	V9~V12
		reserved

Page	Address	Location
10	25Ah	X1
		X2
	25Bh	X3
		X4
	25Ch	X5
		X6
	25Dh	X7
		X8
	25Eh	X9
		X10
	25Fh	X11
		X12
	260h	Y1
		Y2
	261h	Y3
		Y4
	262h	V1~V4
		V5~V8
	263h	V9~V12
		reserved

**Encode Way****1. Ladder(Contact)**

C3	C2	C1	C0	N3	N2	N1	N0
----	----	----	----	----	----	----	----

**1.1.Open/Close & Differential command**C3=0: **A contact(N.O.)**C3=1: **B contact(N.C.)**

C3	C2	C1	C1	N3	N2	N1	N0	
0/1	0	0	0	0	0	0	0	NOP( <b>Open</b> )
				0	0	0	1	SHORT( <b>Close</b> )
				0	0	1	0	reserved
				0	0	1	1	reserved
				0	1	0	0	reserved
				0	1	0	1	reserved
				0	1	1	0	reserved
				0	1	1	1	reserved
				1	0	0	0	D( <b>Upper differential</b> )
				1	0	0	1	d( <b>Lower differential</b> )
				1	0	1	0	reserved
				1	0	1	1	reserved
				1	1	0	0	reserved
				1	1	0	1	reserved
				1	1	1	0	reserved
				1	1	1	1	reserved

**1.2. Input Contact**C3=0: **A contact (N.O.)**C3=1: **B contact (N.C.)**

C3	C2	C1	C1	N3	N2	N1	N0	
0/1	0	0	1	0	0	0	0	S1
				0	0	0	1	S2
				0	0	1	0	S3
				0	0	1	1	S4
				0	1	0	0	S5
				0	1	0	1	S6(Analog Input)
				0	1	1	0	reserved
				0	1	1	1	reserved
				1	0	0	0	reserved
				1	0	0	1	reserved
				1	0	1	0	reserved
				1	0	1	1	reserved
				1	1	0	0	reserved
				1	1	0	1	reserved
				1	1	1	0	reserved
				1	1	1	1	reserved



**1.3.Timer Contact**C3=0: **A contact (N.O.)**C3=1: **B contact (N.C.)**

C3	C2	C1	C1	N3	N2	N1	N0	
0/1	0	1	0	0	0	0	0	T1
				0	0	0	1	T2
				0	0	1	0	T3
				0	0	1	1	T4
				0	1	0	0	T5
				0	1	0	1	T6
				0	1	1	0	T7
				0	1	1	1	T8
				1	0	0	0	reserved
				1	0	0	1	reserved
				1	0	1	0	reserved
				1	0	1	1	reserved
				1	1	0	0	reserved
				1	1	0	1	reserved
				1	1	1	0	reserved
				1	1	1	1	reserved

**1.4.Counter Contact**C3=0: **A contact (N.O.)**C3=1: **B contact (N.C.)**

C3	C2	C1	C1	N3	N2	N1	N0	
0/1	0	1	1	0	0	0	0	C1
				0	0	0	1	C2
				0	0	1	0	C3
				0	0	1	1	C4
				0	1	0	0	reserved
				0	1	0	1	reserved
				0	1	1	0	reserved
				0	1	1	1	reserved
				1	0	0	0	reserved
				1	0	0	1	reserved
				1	0	1	0	reserved
				1	0	1	1	reserved
				1	1	0	0	reserved
				1	1	0	1	reserved
				1	1	1	0	reserved
				1	1	1	1	reserved

**1.5. Auxiliary Coil Contact**C3=0: **A contact (N.O.)**C3=1: **B contact (N.C.)**

C3	C2	C1	C1	N3	N2	N1	N0	
0/1	1	0	0	0	0	0	0	M1
				0	0	0	1	M2
				0	0	1	0	M3
				0	0	1	1	M4
				0	1	0	0	M5
				0	1	0	1	M6
				0	1	1	0	M7
				0	1	1	1	M8
				1	0	0	0	M9
				1	0	0	1	MA
				1	0	1	0	MB
				1	0	1	1	MC
				1	1	0	0	MD
				1	1	0	1	ME
				1	1	1	0	MF
				1	1	1	1	reserved

**1.6. Run Control Contact**C3=0: **A contact (N.O.)**C3=1: **B contact (N.C.)**

C3	C2	C1	C1	N3	N2	N1	N0	
0/1	1	0	1	0	0	0	0	F1
				0	0	0	1	F2
				0	0	1	0	F3
				0	0	1	1	F4
				0	1	0	0	F5
				0	1	0	1	F6
				0	1	1	0	F7
				0	1	1	1	F8
				1	0	0	0	reserved
				1	0	0	1	reserved
				1	0	1	0	reserved
				1	0	1	1	reserved
				1	1	0	0	reserved
				1	1	0	1	reserved
				1	1	1	0	reserved
				1	1	1	1	reserved

**1.7. ANALOG Comparing Contact/ ENCODER Comparing Contact**C3=0: **A contact (N.O.)**C3=1: **B contact (N.C.)**

C3	C2	C1	C1	N3	N2	N1	N0	
0/1	1	1	0	0	0	0	0	G1 (ANALOG Comparator)
				0	0	0	1	G2 (ANALOG Comparator)
				0	0	1	0	G3 (ANALOG Comparator)
				0	0	1	1	G4 (ANALOG Comparator)
				0	1	0	0	reserved
				0	1	0	1	reserved
				0	1	1	0	reserved
				0	1	1	1	reserved
				1	0	0	0	H1 (ENCODER Comparator)
				1	0	0	1	H2 (ENCODER Comparator)
				1	0	1	0	H3 (ENCODER Comparator)
				1	0	1	1	H4 (ENCODER Comparator)
				1	1	0	0	reserved
				1	1	0	1	reserved
				1	1	1	0	reserved
				1	1	1	1	reserved

**1.8. Output Coil Contact**C3=0: **A contact (N.O.)**C3=1: **B contact (N.C.)**

C3	C2	C1	C1	N3	N2	N1	N0	
0/1	1	1	1	0	0	0	0	Q1
				0	0	0	1	Q2
				0	0	1	0	reserved
				0	0	1	1	reserved
				0	1	0	0	reserved
				0	1	0	1	reserved
				0	1	1	0	reserved
				0	1	1	1	reserved
				1	0	0	0	reserved
				1	0	0	1	reserved
				1	0	1	0	reserved
				1	0	1	1	reserved
				1	1	0	0	reserved
				1	1	0	1	reserved
				1	1	1	0	reserved
				1	1	1	1	reserved

**2. Ladder(Coil)**

C3	C2	C1	C0	N3	N2	N1	N0
----	----	----	----	----	----	----	----

**2.1 Output Coil**

C3	C2	Other Instructions Symbol
----	----	---------------------------

0	0	[
0	1	▲
1	0	▼
1	1	P

C3	C2	C1	C0	N3	N2	N1	N0	
0/1	0/1	0	0	0	0	0	0	open
				0	0	0	1	reserved
				0	0	1	0	reserved
				0	0	1	1	reserved
				0	1	0	0	reserved
				0	1	0	1	reserved
				0	1	1	0	reserved
				0	1	1	1	reserved
				1	0	0	0	Q1 (Output coil)
				1	0	0	1	Q2 (Output coil)
				1	0	1	0	reserved
				1	0	1	1	reserved
				1	1	0	0	reserved
				1	1	0	1	reserved
				1	1	1	0	reserved
				1	1	1	1	reserved

**2.2 Auxiliary Coil**

C3	C2	C1	C0	N3	N2	N1	N0	
0/1	0/1	0	1	0	0	0	0	M1 (Auxiliary coil)
				0	0	0	1	M2
				0	0	1	0	M3
				0	0	1	1	M4
				0	1	0	0	M5
				0	1	0	1	M6
				0	1	1	0	M7
				0	1	1	1	M8
				1	0	0	0	M9
				1	0	0	1	MA
				1	0	1	0	MB
				1	0	1	1	MC
				1	1	0	0	MD
				1	1	0	1	ME
				1	1	1	0	MF
				1	1	1	1	reserved

**2.3 Timer Coil**

C3	C2	C1	C0	N3	N2	N1	N0	
0	0	1	0	0	0	0	0	T1
				0	0	0	1	T2
				0	0	1	0	T3
				0	0	1	1	T4
				0	1	0	0	T5
				0	1	0	1	T6
				0	1	1	0	T7
				0	1	1	1	T8
				1	0	0	0	reserved
				1	0	0	1	reserved
				1	0	1	0	reserved
				1	0	1	1	reserved
				1	1	0	0	reserved
				1	1	0	1	reserved
				1	1	1	0	reserved
				1	1	1	1	reserved

**2.4 Counter Coil**

C3	C2	C1	C0	N3	N2	N1	N0	
0	1	1	0	0	0	0	0	C1
				0	0	0	1	C2
				0	0	1	0	C3
				0	0	1	1	C4
				0	1	0	0	reserved
				0	1	0	1	reserved
				0	1	1	0	reserved
				0	1	1	1	reserved
				1	0	0	0	reserved
				1	0	0	1	reserved
				1	0	1	0	reserved
				1	0	1	1	reserved
				1	1	0	0	reserved
				1	1	0	1	reserved
				1	1	1	0	reserved
				1	1	1	1	reserved

**2.5 Analog Coil**

C3	C2	C1	C0	N3	N2	N1	N0	
1	0	1	0	0	0	0	0	G1
				0	0	0	1	G2
				0	0	1	0	G3
				0	0	1	1	G4
				0	1	0	0	reserved
				0	1	0	1	reserved
				0	1	1	0	reserved
				0	1	1	1	reserved
				1	0	0	0	reserved
				1	0	0	1	reserved
				1	0	1	0	reserved
				1	0	1	1	reserved
				1	1	0	0	reserved
				1	1	0	1	reserved
				1	1	1	0	reserved
				1	1	1	1	reserved

**2.6 Encoder Coil**

C3	C2	C1	C0	N3	N2	N1	N0	
1	1	1	0	0	0	0	0	H1
				0	0	0	1	H2
				0	0	1	0	H3
				0	0	1	1	H4
				0	1	0	0	reserved
				0	1	0	1	reserved
				0	1	1	0	reserved
				0	1	1	1	reserved
				1	0	0	0	reserved
				1	0	0	1	reserved
				1	0	1	0	reserved
				1	0	1	1	reserved
				1	1	0	0	reserved
				1	1	0	1	reserved
				1	1	1	0	reserved
				1	1	1	1	reserved

**2.7 Run Control Coil**

C3	C2	C1	C0	N3	N2	N1	N0	
0	0	1	1	0	0	0	0	F1
				0	0	0	1	F2
				0	0	1	0	F3
				0	0	1	1	F4
				0	1	0	0	F5
				0	1	0	1	F6
				0	1	1	0	F7
				0	1	1	1	F8
				1	0	0	0	reserved
				1	0	0	1	reserved
				1	0	1	0	reserved
				1	0	1	1	reserved
				1	1	0	0	reserved
				1	1	0	1	reserved
				1	1	1	0	reserved
				1	1	1	1	reserved

note:

C3	C2	C1	C0	
0	0	0	0	[Q
0	0	0	1	[M
0	0	1	0	[T
0	0	1	1	[F
0	1	0	0	▲Q
0	1	0	1	▲M
0	1	1	0	[C
0	1	1	1	
1	0	0	0	▼Q
1	0	0	1	▼M
1	0	1	0	[G
1	0	1	1	
1	1	0	0	PQ
1	1	0	1	PM
1	1	1	0	[H
1	1	1	1	



**B. FUNCTION BLOCK****1. Timer (10Byte)-----264h~ 28Bh, total: 8 groups**

Timer Mode/Timer Unit--

Timer reset contact-----

Operation value-----

Operation value (MODE7)

Present value-----

Present value (MODE7)

		TB1	TB0		M2	M1	M0
C3	C2	C1	C0	N3	N2	N1	N0
D15'	D14'	D13'	D12'	D11'	D10'	D9'	D8'
D7'	D6'	D5'	D4'	D3'	D2'	D1'	D0'
D15	D14	D13	D12	D11	D10	D9	D8
D7	D6	D5	D4	D3	D2	D1	D0
D15	D14	D13	D12	D11	D10	D9	D8
D7	D6	D5	D4	D3	D2	D1	D0
D15	D14	D13	D12	D11	D10	D9	D8
D7	D6	D5	D4	D3	D2	D1	D0

**Timer reset contact: S1~f8**

off: continue to time

on: reset the value and let the timer OFF

the same as the code method(refer to page3)

**TB : Timer Unit**

Tb1 Tb0

0 1 0.1 sec/unit

1 0 1 sec/unit

1 1 1 min/unit

**M : Timer Mode**

M2 M1 M0

0 0 1 ON-DELAY A Mode

0 1 0 ON-DELAY B Mode

0 1 1 OFF-DELAY A Mode

1 0 0 OFF-DELAY B Mode

1 0 1 FLASH A Mode

1 1 0 FLASH B Mode

1 1 1 FLASH C Mode

Timer1	264h~268h	Timer5	278h~27Ch
Timer2	269h~26Dh	Timer6	27Dh~281h
Timer3	26Eh~272h	Timer7	282h~286h
Timer4	273h~277h	Timer8	287h~28Bh

**2. Counter (8Byte)----- 28Ch~29Bh, *total: 4 groups***

	0	0	0	0	0	0	0
Counter Mode-----	0	0	0	0	0	M2	M1
Counting up/down contact---	C3	C2	C1	C0	N3	N2	N1
Counter Reset Contact-----	C3	C2	C1	C0	N3	N2	N1
Operation Value-----	D15'	D14'	D13'	D12'	D11'	D10'	D9'
	D7'	D6'	D5'	D4'	D3'	D2'	D1'
Present value-----	D15	D14	D13	D12	D11	D10	D9
	D7	D6	D5	D4	D3	D2	D1

**Counter Reset Contact: S1~f8**

OFF: Counting UP

ON: Counting DOWN

the same as the code method(refer to page3)

**Counting up/down Contact: S1~f8**

OFF: continue to count

ON: reset the value and let the counter OFF

the same as the code method(refer to page3)

**M : Counter Mode**

M2	M1	M0	
0	0	1	Mode 1
0	1	0	Mode 2
0	1	1	Mode 3
1	0	0	Mode 4

Counter1	28Ch~28Fh
Counter2	290h~293h
Counter3	294h~297h
Counter4	298h~29Bh

**3. Encoder Compare (10Byte)----- 2ACh~ 2BFh, total: 4 groups**

Encoder Control Mode----

Counting up/down selec.--

Encoder Reset Contact-----

C Encoder division-----

A2, Comparison Value-----

A1, Encoder Present Value

							M
C3	C2	C1	C0	N3	N2	N1	N0
C3	C2	C1	C0	N3	N2	N1	N0
D15	D14	D13	D12	D11	D10	D9	D8
D7	D6	D5	D4	D3	D2	D1	D0
D15'	D14'	D13'	D12'	D11'	D10'	D9'	D8'
D7'	D6'	D5'	D4'	D3'	D2'	D1'	D0'
D15	D14	D13	D12	D11	D10	D9	D8
D7	D6	D5	D4	D3	D2	D1	D0

**M : Control Mode**0: Control Mode 1, Encoder Comparison function ( $A1 \geq A2$  comparison output)1: Control Mode 2, Encoder Comparison function ( $A1 \leq A2$  comparison output)**Encoder Reset contact: S1~f8**

OFF: not RESET

ON: RESET

the same as the code method(refer to page3)

**Counting up/down selec.: S1~f8**

OFF : Counting Up

ON : Counting Down

the same as the code method(refer to page3)

Encoder1	2ACh~2B0h
Encoder2	2B1h~2B5h
Encoder3	2B6h~2BAh
Encoder4	2BBh~2BFh

**4. Analog Compare (6Byte)-----2C0h~ 2CBh, total: 4 groups**

Analog Compare Mode-----

Analog Compare Selec.-----

Setting reference comparison value (upper limit)

Setting reference comparison value (lower limit)

					M2	M1	M0
					V2	V1	V0
D15'	D14'	D13'	D12'	D11'	D10'	D9'	D8'
D7'	D6'	D5'	D4'	D3'	D2'	D1'	D0'
D15	D14	D13	D12	D11	D10	D9	D8
D7	D6	D5	D4	D3	D2	D1	D0

**M : Analog Comparison Mode**

M2	M1	M0
----	----	----

0	0	1	Mode1 (input comparison value<=reference comparison value(lower limit),Analog comparator ON)
0	1	0	Mode2 (input comparison value>=reference comparison value(upper limit),Analog comparator ON)
0	1	1	Mode3(input comparison between upper and lower limit,Analog comparator ON)

**V : selection of analog comparison value**

V2	V1	V0
----	----	----

0	0	1	V1 : setting frequency
0	1	0	V2 : operation frequency
0	1	1	V3 : AI1 input value
1	0	0	V4 : AI2 input value
1	0	1	V5 : KEYPAD input value
1	1	0	V6 : operation current
1	1	1	V7 : torque value

Analog1	2C0h~2C2h
Analog2	2C3h~2C5h
Analog3	2C6h~2C8h
Analog4	2C9h~2CBh

**5. Running Instruction (12Byte)----- 2CCh~ 2FBh, total: 8 groups**

Step speed control-----

Step speed control contact--

Forward/Reverse contact---

Acc. time-----

Dec. time-----

Setting frequency-----

Step frequency-----

						M4	M3
						M2	M1
C3	C2	C1	C0	N3	N2	N1	N0
C3	C2	C1	C0	N3	N2	N1	N0
D15	D14	D13	D12	D11	D10	D9	D8
D7	D6	D5	D4	D3	D2	D1	D0
D15	D14	D13	D12	D11	D10	D9	D8
D7	D6	D5	D4	D3	D2	D1	D0
D15'	D14'	D13'	D12'	D11'	D10'	D9'	D8'
D7'	D6'	D5'	D4'	D3'	D2'	D1'	D0'
D15	D14	D13	D12	D11	D10	D9	D8
D7	D6	D5	D4	D3	D2	D1	D0

**Step Speed Control Contact: S1~f8**

off : run by setting frequency

on : run by step frequency

the same as the code method(refer to page3)

**Forward/Reverse Contact: S1~f8**

off : forward(FWD)

on : reverse(REV)

the same as the code method(refer to page3)

**Frequency Source:**

M4 M3

0 0 setting frequency is constant

0 1 setting frequency is V3

1 0 setting frequency is V5

**Step Frequency Source:**

M2 M1

0 0 step frequency is constant

0 1 step frequency is V3

1 0 step frequency is V5

Control1	2CCh~2D1h
Control2	2D2h~2D7h
Control3	2D8h~2DDh
Control4	2DEh~2E3h

Control5	2E4h~2E9h
Control6	2EAh~2EFh
Control7	2F0h~2F5h
Control8	2F6h~2FBh

**6. *PLC RUN*→330h~ *Clear MEMORY*→331h**

RUN&amp;STOP-----

CLEAR PLC MEMORY--

X	X	X	X	X	X	X	X
X	X	X	X	X	X	X	RS
X	X	X	X	X	X	X	X
X	X	X	X	X	X	X	C

**RS : PLC *Run command* (Bit 0)**0 : PLC *Stop*1 : PLC *Run***C : PLC ALL MEMORY CLEAR(Bit 0)**

0 : Disable

1 : Enable