

PBX-RF

RF-Isolated Parallel Port Breakout Board

Figure 2: Board Diagram

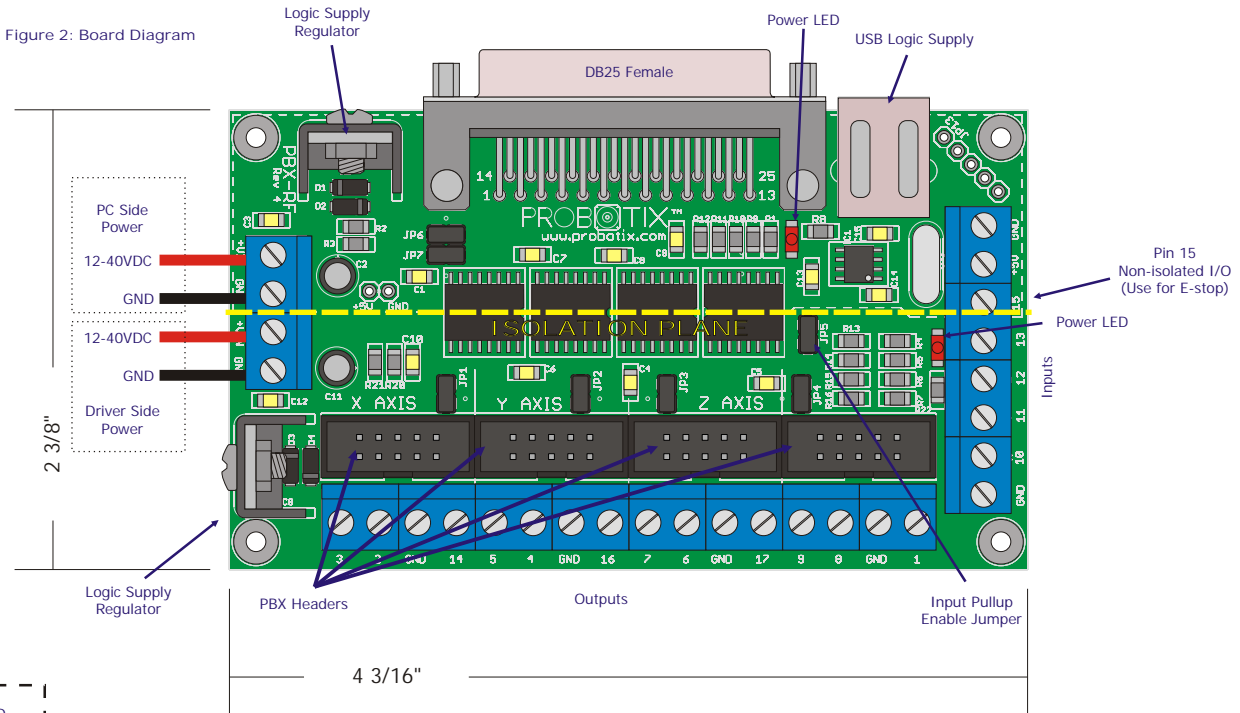


Figure 3: Jumper Functions

JP1	X Axis Power
JP2	Y Axis Power
JP3	Z Axis Power
JP4	A Axis Power
JP5	Input Pull-ups Enable
JP6	Voltage Regulator Isolation
JP7	Input Enable

Figure 1: PBX Header Pin Diagram

